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Editor's Note: I was recently perusing a new book called Design Recipes for FPGAs that was written by Peter Wilson and published by Newnes (ISBN-13: 978-0750668453). This is a rather interesting "Cook Book" jam-packed with "Design Recipes". Part 1 provides primers for FPGAs, VHDL, and standard design flows.

*Design Recipes for FPGAs - A Simple VGA Interface | EE Times*

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Chapter 1 Introduction  
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Peter Wilson, in *Design Recipes for FPGAs (Second Edition)*, 2016. 16.1

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*Design Optimization - an overview | ScienceDirect*

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Design Recipes for FPGAs doesn't talk about this, probably since it's aimed at people building hardware. Be warned; HDLs are very different from programming languages. FPGA design has a number of steps - the testbench, compilation, synthesis, routing, etc. and Mr. Wilson does take the reader through the design flow.

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