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~~Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z~~

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book Review Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is Q.

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4.1: Consider the combinational circuit shown in Fig.

P4.1.(a)* Derive the Boolean expressions

for Digital Design: Q.

1.13: Do the following conversion problems:

(a) Convert decimal 27.315 to binary Q.

5.8: Derive the state table and the state diagram of the sequential circuit

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shown in Fig. P5.8

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Unit 04 -

Combinational Logic -
Part 03 - Design

Procedure Q. 5.4: A

PN flip-flop has four
operations: clear to 0,
no change,

complement, and set
to 1, when DIGITAL

SYSTEM DESIGN

OVERVIEW Q. 6.10:

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Q. 7.19: Tabulate the
PLA programming
table for the four

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Boolean functions

listed below. Q. 3.13:

Simplify the following expressions to (1)

sum-of-products and

(2) products-of-sums

Q. 3.15: Simplify the

following Boolean

function F , together

with the don't-care

conditions d , and Q.

5.1: The D latch of

Fig. 5.6 is constructed

with four NAND gates

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Sequential Circuits
L03 Q.5.20: Design
the sequential circuit
specified by the state
diagram of Fig. 5.19
using T flip-flops Q.
3.9: Find all the prime
implicants for the
following Boolean

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Morris Mano is from California State University, Los Angeles. He has written a number of books including Logic and Computer Design Fundamentals: International Version, Computer Engineering: Hardware Design, Digital Design.

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Procedure: Task 1:
Our first task was to create a logic diagram of a 4-bit binary adder, which consisted of just one chip titled “ 7483N ” , along with creating a truth table. I connected the A1-A4 to either the voltage or ground

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